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Kikukawa, H.; Tomishima, S.; Tsuji, T.; Kawasaki, T.; Sakamoto, S.; Ishikawa Abe, W.; Tanizaki, H.; Kato, H.; Uchikoba, T.; Inokuchi, T.; Senoh, M.; Fukusi Y.; Nirro, M.; Maruta, M.; Shibayama, A.; Ooishi, T.; Takahashi, K.; Hidaka, F. Solid-State Circuits, IEEE Journal of , Volume: 37 , Issue: 7 , July 2002 Pages:932 - 940

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L24	5220	mpeg2	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/03/04 14:05
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S5	2	S3 and S4	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/03/03 16:45
S6	3866	insert\$4 near2 (intensit\$4 degree)	US-PGPUB; USPAT; IBM_TDB	OR	ON	2005/03/03 16:45
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